

943

09/682131

Class	Subclass
ISSUE CLASSIFICATION	

PATENT DATE

**SCANNED**

363 Q.A. [Signature]

APPLICATION NO. 09/682131	CONT/PRIOR F	CLASS 257	SUBCLASS 724	ART UNIT <del>2851</del> 8815	EXAMINER Chu
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## APPLICANTS

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## TITLE

Semiconductor integrated circuit device with EMI prevention structure

PTO-2040  
12/99

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

☐ Continued on Issue Slip Inside File Jacket

**TERMINAL  
DISCLAIMER**

## DRAWINGS

**Sheets Drwg.**

**Figs. Drwg.**

Print Fig.

## CLAIMS ALLOWED

### Total Claims

**Print Claim for O.G.**

☐ The term of this patent subsequent to \_\_\_\_\_ (date) has been disclaimed.

(Assistant Examiner)

(Date)

☒ The term of this patent shall not extend beyond the expiration date of U.S. Patent No. \_\_\_\_\_

(Primary Examiner)

(Date)

**NOTICE OF ALLOWANCE MAILED**

## ISSUE FEE

**Amount Due**

Date Paid

**ISSUE BATCH NUMBER**

☐ The terminal \_\_\_\_ months of this patent have been disclaimed.

(Legal Instruments Examiner)

(Date)

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(Rev. 6/89)

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(FACE)